

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Hiromitsu KAWAMURA**

Serial No.: **Not Yet Assigned**

Filed: **February 1, 2002**

For: **SEMICONDUCTOR DEVICE AND CHIP CARRIER**

J1046 U.S. PTO  
10/060323  
02/01/02

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Sir:

February 1, 2002

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the reference listed on the attached PTO-1449.

A copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,  
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Enclosures: PTO-1449; Reference (1)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. <b>020122</b>	Serial No. New Appln.
	Applicant(s): <b>Hiromitsu KAWAMURA</b>	
	Filing Date: <b>February 1, 2002</b>	Group Art Unit:

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### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)		
_____	AF	6-61012	03/04/94	Japan	Abstract & Discussed in the Spec.
_____	AG				
_____	AH				
_____	AI				
_____	AJ				

### OTHER DOCUMENTS

_____	AK	
_____	AL	

Examiner	Date Considered
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